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11-173

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2815
Serial No. : 09/736,724 Examiner : Paul E. Brock
Filed : December 13, 2000 Confirmation No.: 8339
Title : INSULATED GATE SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

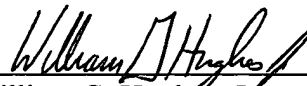
REQUEST FOR INITIALED PTO FORMS 1449 & 892

Upon reviewing the file, applicants noted that they have not received an initialed copy of the enclosed PTO Forms 1449 & 892 that accompanied an information disclosure statement filed with the application on December 13, 2000.

Applicants' records show that this information disclosure statement complied with 37 CFR 1.97. Thus, it is respectfully requested that the examiner initial and return these forms as soon as possible.

Respectfully submitted,

Date: October 27, 2003



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